# Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science 

6.002 - Circuits \& Electronics<br>Spring 2007

Final Exam

21 May 2007

Name: $\qquad$

- There are 31 pages in this final, including this cover page. Please check that you have them all.
- Please write your name in the space provided above, and circle the name of your recitation instructor along with the time of your recitation.
- IMPORTANT: The problems in this exam vary in difficulty; moreover, questions of different levels of difficulty are distributed throughout the exam. If you find yourself spending a long time on a question, consider moving on to later problems in the exam, and then working on the challenging problems after you have finished all of the easier ones.
- Do your work for each question within the boundaries of that question, or on the back of the preceding page. When finished, enter your answer to each question in the corresponding answer box that follows the question.
- Remember to include the sign and units for all numerical answers.
- This is a closed-book exam, but you may use a calculator and three double-sided pages of notes.
- You have 3 hours to complete this final.
- Good luck!

| 1 A. | 1 B. | 2. | 3. | 4. |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 5. | 6. | 7. | 8. | 9. |  |
| 10. | 11. | 12 A. | 12 B. | 13. |  |
| 14. | 15. | 16. | 17. | 18. |  |
|  | 19 A. | 19 B. | 19 C. | 19 D. |  |
| Final Score: |  |  |  |  |  |

## Problem 1: 8 points



Figure 1.

A MOSFET (shown in Figure 1) operating in the triode region has a characteristic $i_{D S}$ relationship which depends on both $v_{D S}$ and $v_{G S}$ :

$$
i_{D S}=K\left[\left(v_{G S}-V_{T}\right) v_{D S}-v_{D S}^{2} / 2\right]
$$

where $v_{G S}>V_{T}$ and $v_{D S} \leq v_{G S}-V_{T}$. The small-signal relationship between $i_{d s}, v_{g s}$, and $v_{d s}$ can be expressed by an equation of the form

$$
i_{d s}=A v_{g s}+B v_{d s}
$$

where $A$ and $B$ are constants. Assume that the device is biased at an operating point $\left(V_{G S}, V_{D S}\right)$.
(1A) (4 points) Draw the 2-element small-signal model for the device which is operating in the triode region. Express the element values in terms of $A$ and $B$.

## Small-signal equivalent circuit:

(1B) (4 points) Find the values of the constants $A$ and $B$ in the two-element small signal model for the MOSFET operating in the triode region,

$$
i_{d s}=A v_{g s}+B v_{d s}
$$

Formulate your answers in terms of the variables $K, V_{G S}, V_{T}$, and $V_{D S}$.
$A=$
$B=$

## Problem 2: 4 points

A and B are 1 bit numbers. Each of them can take on the positive integer values 0 or 1 . Implement an adder which takes the two inputs A and B and produces a 1 bit sum output Z . The sum output Z must saturate at 1 , which means that if the sum is greater than 1 , the circuit must output a 1.

You may use as many inverters, NAND gates and NOR gates as you like in your circuit.

## Your adder design:

Problem 3: 4 points
Consider the logic function $O$ described by the following truth table.

| $A$ | $B$ | $C$ | $O(A, B, C)$ |
| :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Show how to implement the function $O$ using only two-input NAND gates and inverters. (Hint: Remember the relations indicated by Figure 2.)


Figure 2.

Problem 3 (continued):

Implementation of $O$ :

Problem 4: 4 points


Figure 3.

In the circuit in Figure 3, the current reading from the ammeter is 4 mA with the switch open, and 3 mA with the switch closed, where $i_{D S}=(K / 2)\left(v_{G S}-V_{T}\right)^{2}$. What are $V_{T}$ and $K$ ?

$$
V_{T}=
$$

$K=$

Problem 5: 4 points


Figure 4.
Let the network depicted in Figure 4 have a frequency response given by

$$
\frac{v_{o}}{v_{i}}=\frac{1}{1+j \omega K}
$$

where K is positive. The magnitude and phase of this frequency response as a function of $\omega$ are illustrated by the two plots below. Find the values of $\omega_{c}, m, \phi\left(\omega_{c}\right)$, and $\phi(\infty)$ in terms of the circuit parameters $R$ and $C$.



Problem 5 (continued):

$$
\begin{aligned}
& \omega_{c}= \\
& m= \\
& \phi\left(\omega_{c}\right)= \\
& \phi(\infty)=
\end{aligned}
$$

Problem 6: 4 points


Figure 5.

Consider the circuit in Figure 5 with two inputs $V(t)=\Lambda \delta(t)$ and $I(t)=Q \delta(t)$. The inductor and capacitor have zero initial state, i.e. $v_{C}\left(t=0^{-}\right)=0$ and $i_{L}\left(t=0^{-}\right)=0$. What are the inductor current $i_{L}$ and the capacitor voltage $v_{C}$ at $t=0^{+}$? At what frequency $\omega_{o s c}$ will the circuit oscillate?

$$
\begin{aligned}
& v_{C}\left(0^{+}\right)= \\
& i_{L}\left(0^{+}\right)= \\
& \omega_{o s c}=
\end{aligned}
$$

Problem 7: 4 points


Figure 6.

What is the time constant $\tau$ for the circuit depicted in Figure 6 ?
$\tau=$

(a)

(b)

Figure 7.

The network shown in Figure 7a contains an ideal diode. For the network, plot vout $(t)$ when $v_{I N}(t)$ is the pulse shown in Figure 7b. The capacitor is initially discharged. Please label all important features of the plot, such as amplitude, time constant, and so forth.

For convenience, we have illustrated the $v_{D}-i_{D}$ relationship of the ideal diode in Figure 8b, for the diode terminal variables as defined in Figure 8a.

(a)

(b)

Figure 8.

Problem 8 (continued):


Problem 9: 4 points


Figure 9.

What is the time constant $\tau$ for the circuit depicted in Figure 9?
$\tau=$

Inlet Inc. manufactures two types of inverters - the SX model and the EX model - which satisfy the following static disciplines.

Inverter SX satisfies a static discipline with the following voltage levels:

$$
\begin{array}{ll}
V_{O H}=4 V & V_{O L}=0.5 V \\
V_{I H}=2 V & V_{I L}=1 V
\end{array}
$$

Similarly, Inverter EX satisfies a static discipline with these voltage levels:

$$
\begin{array}{ll}
V_{O H}=5 V & V_{O L}=0.7 V \\
V_{I H}=1.2 V & V_{I L}=1 V
\end{array}
$$

Inlet would like to bid on a contract for buffers issued by QuellCom. The buffers need to operate in a system which follows a static discipline with the following voltage levels:

$$
\begin{array}{ll}
V_{O H}=4.55 \mathrm{~V} & V_{O L}=0.8 V \\
V_{I H}=1.5 \mathrm{~V} & V_{I L}=1 V
\end{array}
$$

As an application engineer at Inlet you are tasked with determining whether a pair of inverters connected in series can satisfy QuellCom's static discipline. For each of the inverter pairs shown below, indicate whether the circuit can serve as a buffer in QuellCom's systems, or explain in a sentence why not.

Is SX-SX a satisfactory buffer? YES ..... NOIf not, explanation:Is SX-EX a satisfactory buffer?YESNOIf not, explanation:Is EX-SX a satisfactory buffer?YESNO
If not, explanation:
Is EX-EX a satisfactory buffer?
YES
If not, explanation:

Determine an expression for $v_{O}$ in terms of $v_{I}$ for the circuit in Figure 10. Assume the MOSFET is operating in its saturation region, with $i_{D S}=(K / 2)\left(v_{G S}-V_{T}\right)^{2}$, and that $v_{I}<0$.


Figure 10.

$$
v_{O}=
$$

Problem 12: 8 points


Figure 11.
Q. Rius is an engineer at Inlet Inc. Tasked with building a filter, Rius comes up with the op-amp circuit shown below, and applies the input signal $v_{I}=A \sin (\omega t)$.
(12A) (4 points) The output $v_{o}(t)$ is of the form

$$
v_{o}(t)=V_{o} \sin (\omega t+\theta)
$$

Determine the value of $V_{o}$ and $\theta$ in terms of $A, R, C_{1}, C_{2}$, and $\omega$.

$$
V_{o}=
$$

$$
\theta=
$$

## Problem 12 (continued):

(12B) (4 points) Experimenting with his circuit, Rius notices that the resistance R is so large compared to the impedances of C 1 and C 2 that it has little observable effect on the output even when he doubles or quadruples the value of R. Excited with this observation, Rius removes R from his circuit in an attempt to reduce the number of components. To his dismay, the circuit ceases to work as before. Explain in a sentence or two the reason for the circuit's failure.

## Explanation:

Consider the circuit shown in Figure 12. The parameters characterizing the behavior of the MOSFET are $K=2 m A / V^{2}$, and $V_{T}=1 V$, and $i_{D S}=(K / 2)\left(v_{G S}-V_{T}\right)^{2}$.


Figure 12.

Determine the value of $V_{O}$, the DC component of the output voltage. Assume that $v_{i}=0$.

$$
V_{O}=
$$

Consider the circuit shown in Figure 13. The parameters characterizing the behavior of the MOSFET are $K=2 m A / V^{2}$, and $V_{T}=1 V$, and $i_{D S}=(K / 2)\left(v_{G S}-V_{T}\right)^{2}$.


Figure 13.

Assume that the input for this circuit is $v_{i}(t)=V_{i} \cos (\omega t)$, where $V_{i}$ is a small-signal amplitude, and assume that the MOSFET can be characterized by a transconductance $g_{m}$. The small-signal output $v_{o}(t)$ can be expressed in the following form:

$$
v_{o}(t)=V_{o} \cos (\omega t+\theta)
$$

Determine the values of $V_{o}$ and $\theta$, expressing your answers in terms of $V_{i}, \omega, R, C$, and $g_{m}$.

Problem 14 (continued):

$$
V_{o}=
$$

$$
\theta=
$$



Figure 14.

The circuit depicted in Figure 14a is driven by a signal $v_{I}(t)$ which is a square wave oscillating between +5 V and -5 V with a period of $T$. The initial capacitor voltage $v_{C}(t=0)=0$. The resistor $R_{\infty}$ is included to insure that the op-amp is operating in a stable range, but it is large enough to be ignored in your analysis. Sketch the response $v_{O}(t)$ on the axes provided, labelling all of the important features of the waveform.



Figure 15.

The diodes $D 1$ and $D 2$ in the circuit of Figure 15 are ideal diodes. If the circuit is driven by a signal $v_{i}(t)=V_{i} \cos (\omega t)$, sketch the response $v_{o}(t)$ on the axes provided, labelling all of the important features of the waveform. (The characteristics of the ideal diode were given in Problem 8, in Figure 8.)


Problem 17: 4 points


Figure 16.

Derive an expression for $v_{o}(t)$ in terms of $v_{1}(t)$ and $v_{2}(t)$.

$$
v_{o}(t)=
$$



Figure 17.

For the circuit in Figure 17a, the output waveform $v_{O}(t)$ is illustrated in Figure 17b. The important features of the waveform are denoted $A, B$, and $T$; please find expressions for these variables in terms of $R, R_{1}, C$, and the power supply rails of the op-amp, which are $+V_{S}$ and $-V_{S}$.

$$
A=
$$

$B=$
$T=$

## Problem 19: 20 points

A car travels at a constant speed along a bumpy road. As a result of the bumps, the velocity of the car's axle perpendicular to the road is $V(t)$. Taking into account the dynamics of the car's suspension system, the equation of motion for the body of the car (the chassis) can be formulated as follows:

$$
\begin{equation*}
M \frac{d u}{d t}+B u(t)+K \int_{-\infty}^{t} u d t=-M \frac{d V}{d t} \tag{1}
\end{equation*}
$$

where $u(t)$ is the velocity of the body of the car (relative to the axle), $M$ is the mass of the car, $K$ is the spring constant of the springs connecting the body to the axle, and $B$ is the coefficient of viscous damping for the shock absorbers.

In this problem, you will be asked to make a circuit model corresponding to the equation of mechanical dynamics from Equation 1, and then you will use the model to investigate the car's motion due to the bumpy road.

Note: Each of the parts of this problem can be worked independently of the others.


Figure 18.
(19A) (5 points) The circuit in Figure 18 is proposed to model Equation 1, where the node voltage $e(t)$ is identified with the car body's velocity $u(t)$. Determine the values of $v_{S}$, $C, L$, and $R$ in terms of the parameters in Equation 1 so that the solution for $e(t)$ will be identical to the solution of Equation 1 for $u(t)$.

Problem 19 (continued):

$$
\begin{aligned}
& v_{S}= \\
& C= \\
& L= \\
& R=
\end{aligned}
$$

(19B) (5 points) Referring to the circuit model, assume that the response is oscillatory.

- What is the frequency of oscillation $\omega_{\text {osc }}$ ?
- How long must one wait for the amplitude of the transient oscillation to decay to $1 / e$ of its initial amplitude?
$\omega_{o s c}=$
$t_{1 / \mathrm{e}}=$
(19C) (5 points) Now assume that it is desirable to avoid oscillatory transient behavior. What is the maximum value of $R$ in this case?
$R_{\text {max }}=$
(19D) (5 points) Once again referring to the circuit, suppose that the source $v_{S}(t)$ is sinusoidal, i.e. $v_{S}(t)=A \sin (\omega t)$. Find an expression for the steady-state voltage $e(t)$.
$e(t)=$

