Lecture 10 MOSFET (III) MOSFET Equivalent Circuit Models

Outline

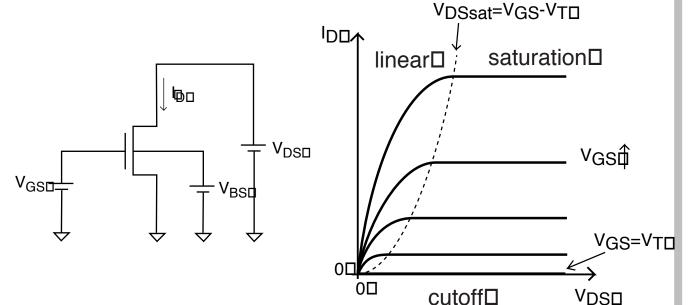
- Low-frequency small-signal equivalent circuit model
- High-frequency small-signal equivalent circuit model

Reading Assignment:

Howe and Sodini; Chapter 4, Sections 4.5-4.6

Large Signal Model for NMOS Transistor

Regimes of operation:



• Cut-off

 $\mathbf{I}_{\mathbf{D}} = \mathbf{0}$

• Linear / Triode:

$$\mathbf{I}_{\mathbf{D}} = \frac{\mathbf{W}}{\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} \left[\mathbf{V}_{\mathbf{GS}} - \frac{\mathbf{V}_{\mathbf{DS}}}{2} - \mathbf{V}_{\mathbf{T}} \right] \bullet \mathbf{V}_{\mathbf{DS}}$$

• Saturation

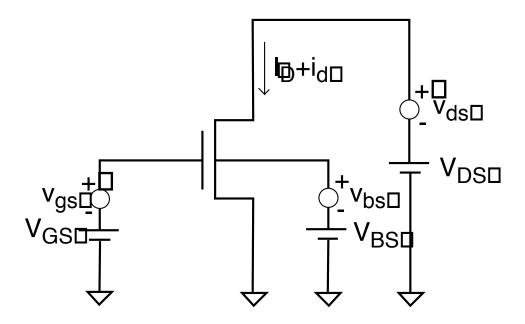
$$I_D = I_{Dsat} = \frac{W}{2L} \mu_n C_{ox} [V_{GS} - V_T]^2 \bullet [1 + \lambda V_{DS}]$$

Effect of back bias

$$\mathbf{V}_{\mathbf{T}}(\mathbf{V}_{\mathbf{BS}}) = \mathbf{V}_{\mathbf{To}} + \gamma \left[\sqrt{-2\phi_{\mathbf{p}} - \mathbf{V}_{\mathbf{BS}}} - \sqrt{-2\phi_{\mathbf{p}}} \right]$$

Small-signal device modeling

In many applications, we are only interested in the response of the device to a *small-signal* applied on top of a bias.



Key Points:

- Small-signal is *small*
 - → response of non-linear components becomes linear
- Since response is linear, lots of linear circuit techniques such as superposition can be used to determine the circuit response.
- Notation: $i_D = I_D + i_d$ --- Total = DC + Small Signal

Mathematically:

$i_D(V_{GS}, V_{DS}, V_{BS}; v_{gs}, v_{ds}, v_{bs}) \approx I_D(V_{GS}, V_{DS}, V_{DS}, V_{BS}) + i_d(v_{gs}, v_{ds}, v_{bs})$

With i_d linear on small-signal drives:

$$\dot{i}_d = g_m v_{gs} + g_o v_{ds} + g_{mb} v_{bs}$$

Define:

 $g_m \equiv transconductance [S]$ $g_o \equiv output \text{ or drain conductance } [S]$ $g_{mb} \equiv backgate transconductance } [S]$

Approach to computing g_m , g_o , and g_{mb} .

$$\begin{aligned} \mathbf{g}_{m} \approx \frac{\partial \mathbf{i}_{D}}{\partial \mathbf{v}_{GS}} \Big|_{Q} \\ \mathbf{g}_{o} \approx \frac{\partial \mathbf{i}_{D}}{\partial \mathbf{v}_{DS}} \Big|_{Q} \\ \mathbf{g}_{mb} \approx \frac{\partial \mathbf{i}_{D}}{\partial \mathbf{v}_{BS}} \Big|_{Q} \end{aligned}$$
$$\mathbf{Q} \equiv \begin{bmatrix} \mathbf{v}_{GS} = \mathbf{V}_{GS}, \mathbf{v}_{DS} = \mathbf{V}_{DS}, \mathbf{v}_{BS} = \mathbf{V}_{BS} \end{bmatrix}$$

Transconductance

In saturation regime:

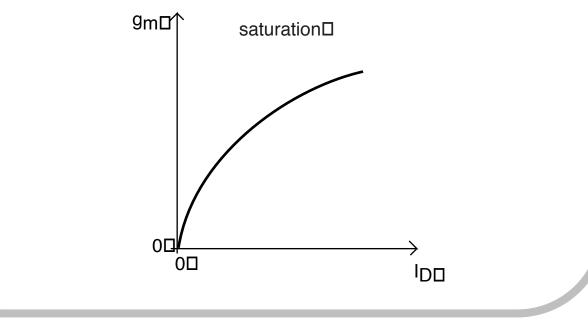
$$i_D = \frac{W}{2L} \mu_n C_{ox} [v_{GS} - V_T]^2 \bullet [1 + \lambda V_{DS}]$$

Then (neglecting channel length modulation) the transconductance is:

$$\mathbf{g}_{\mathbf{m}} = \frac{\partial \mathbf{i}_{\mathbf{D}}}{\partial \mathbf{v}_{\mathbf{GS}}} \bigg|_{\mathbf{Q}} \approx \frac{\mathbf{W}}{\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} (\mathbf{V}_{\mathbf{GS}} - \mathbf{V}_{\mathbf{T}})$$

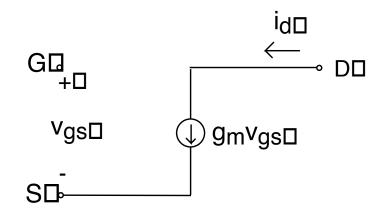
Rewrite in terms of I_D:

$$\mathbf{g}_{\mathbf{m}} = \sqrt{2 \frac{\mathbf{W}}{\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} \mathbf{I}_{\mathbf{D}}}$$

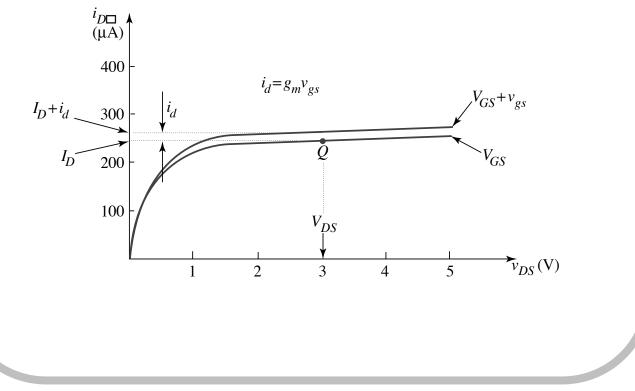


Transconductance (contd.)

Equivalent circuit model representation of g_m **:**







Output conductance

In saturation regime:

$$i_D = \frac{W}{2L} \mu_n C_{ox} \left[v_{GS} - V_T \right]^2 \bullet \left[1 + \lambda v_{DS} \right]$$

Then:

$$\mathbf{g}_{\mathbf{o}} = \frac{\partial \mathbf{i}_{\mathbf{D}}}{\partial \mathbf{v}_{\mathbf{DS}}} \bigg|_{\mathbf{Q}} = \frac{\mathbf{W}}{2\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} (\mathbf{V}_{\mathbf{GS}} - \mathbf{V}_{\mathbf{T}})^2 \bullet \lambda \approx \lambda \mathbf{I}_{\mathbf{D}}$$

Output resistance is the inverse of output conductance:

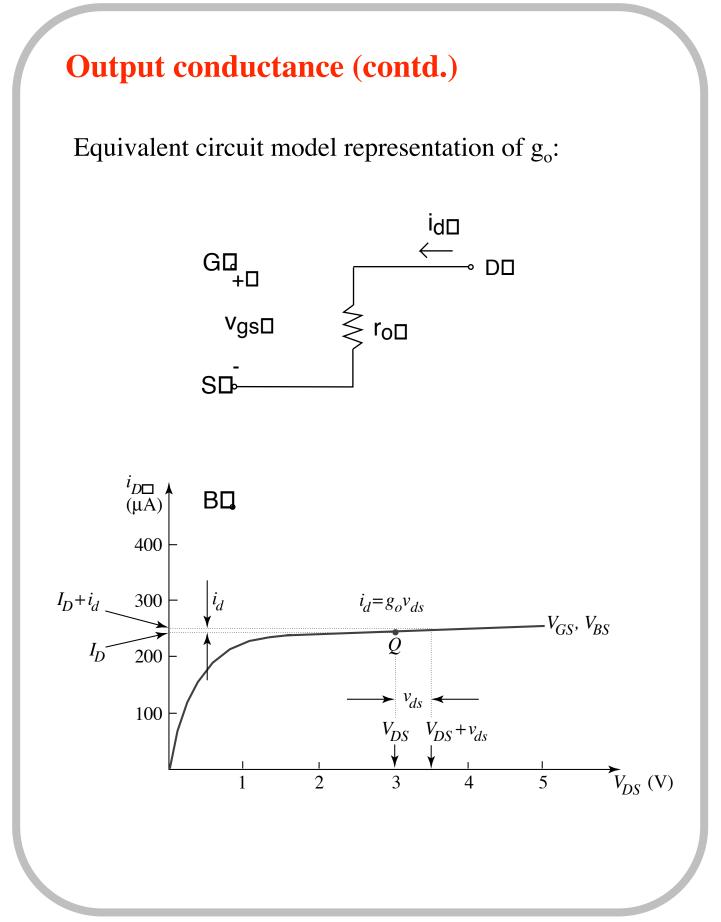
$$\mathbf{r}_{\mathbf{o}} = \frac{1}{\mathbf{g}_{\mathbf{o}}} = \frac{1}{\lambda \mathbf{I}_{\mathbf{D}}}$$

Remember also:

$$\lambda \propto \frac{1}{L}$$

Hence:

$$r_0 \propto L$$



Backgate transconductance

In saturation regime (neglect channel length modulation):

$$\mathbf{i}_{\mathbf{D}} \approx \frac{\mathbf{W}}{2\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} [\mathbf{v}_{\mathbf{GS}} - \mathbf{V}_{\mathbf{T}}]^2$$

Then:

$$\mathbf{g}_{\mathbf{mb}} = \frac{\partial \mathbf{i}_{\mathbf{D}}}{\partial \mathbf{v}_{\mathbf{BS}}} \bigg|_{\mathbf{Q}} = -\frac{\mathbf{W}}{\mathbf{L}} \mu_{\mathbf{n}} \mathbf{C}_{\mathbf{ox}} (\mathbf{V}_{\mathbf{GS}} - \mathbf{V}_{\mathbf{T}}) \bullet \left(\frac{\partial \mathbf{V}_{\mathbf{T}}}{\partial \mathbf{v}_{\mathbf{BS}}} \bigg|_{\mathbf{Q}} \right)$$

Since:

$$\mathbf{V}_{\mathbf{T}}(\mathbf{v}_{\mathbf{BS}}) = \mathbf{V}_{\mathbf{To}} + \gamma \left[\sqrt{-2\phi_{\mathbf{p}} - \mathbf{v}_{\mathbf{BS}}} - \sqrt{-2\phi_{\mathbf{p}}} \right]$$

Then :

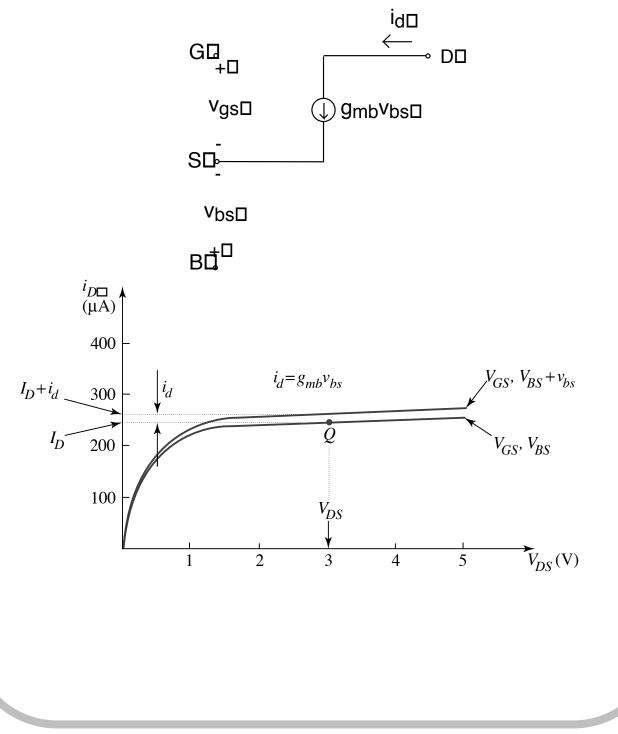
$$\frac{\partial \mathbf{V}_{\mathbf{T}}}{\partial \mathbf{v}_{\mathbf{BS}}} \bigg|_{\mathbf{Q}} = \frac{-\gamma}{2\sqrt{-2\phi_{\mathbf{p}} - \mathbf{V}_{\mathbf{BS}}}}$$

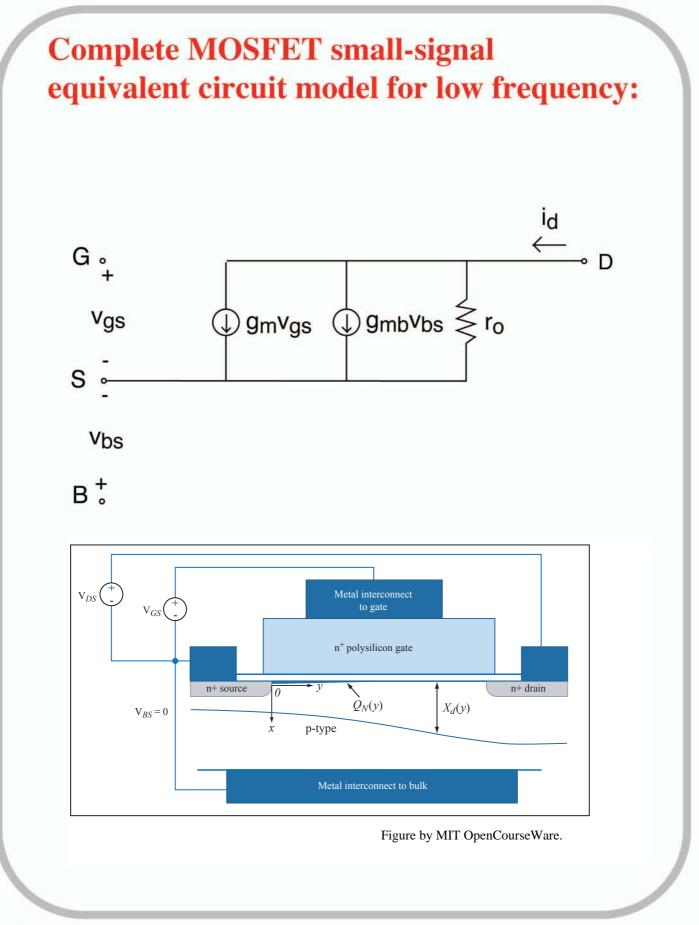
Hence:

$$\mathbf{g}_{\mathbf{mb}} = \frac{\gamma \mathbf{g}_{\mathbf{m}}}{2\sqrt{-2\phi_{\mathbf{p}} - \mathbf{V}_{\mathbf{BS}}}}$$

Backgate transconductance (contd.)

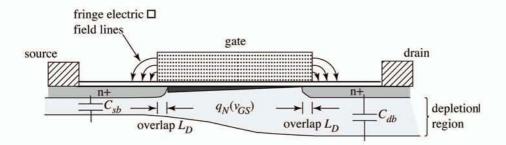
Equivalent circuit representation of g_{mb} :





2. High-frequency small-signal equivalent circuit model

Need to add capacitances. In saturation:



 $C_{gs} \equiv$ channel charge + overlap capacitance, C_{ov} $C_{gd} \equiv$ overlap capacitance, C_{ov} $C_{sb} \equiv$ source junction depletion capacitance (+sidewall) $C_{db} \equiv$ drain junction depletion capacitance (+sidewall)

ONLY Channel Charge Capacitance is intrinsic to device operation. All others are parasitic.

Inversion layer charge in saturation

$$q_N(v_{GS}) = W \int_0^L Q_N(y) \, dy = W \int_0^{v_{GS} - V_T} Q_N(v_C) \bullet \frac{dy}{dv_C} \bullet dv_C$$

Note that q_N is total inversion charge in the channel & $v_C(y)$ is the channel voltage. But:

$$\frac{dv_C}{dy} = -\frac{i_D}{W\mu_n Q_N(v_C)}$$

Then:

$$q_N(v_{GS}) = -\frac{W^2 \mu_n}{i_D} \bullet \int_0^{V_{GS} - V_T} \left[Q_N(v_C) \right]^2 \bullet dv_C$$

Remember:

$$Q_N(v_C) = -C_{ox} [v_{GS} - v_C(y) - V_T]$$

Then:

$$q_N(v_{GS}) = -\frac{W^2 \mu_n C_{ox}^2}{i_D} \bullet \int_{0}^{v_{GS} - V_T} \left[v_{GS} - v_C(y) - V_T \right]^2 \bullet dv_C$$

Inversion layer charge in saturation (contd.)

Do integral, substitute i_D in saturation and get:

$$\mathbf{q}_{\mathbf{N}}(\mathbf{v}_{\mathbf{GS}}) = -\frac{2}{3}\mathbf{WLC}_{\mathbf{ox}}(\mathbf{v}_{\mathbf{GS}} - \mathbf{V}_{\mathbf{T}})$$

Gate charge:

$$\mathbf{q}_{\mathbf{G}}(\mathbf{v}_{\mathbf{GS}}) = -\mathbf{q}_{\mathbf{N}}(\mathbf{v}_{\mathbf{GS}}) - \mathbf{Q}_{\mathbf{B},\max}$$

Intrinsic gate-to-source capacitance:

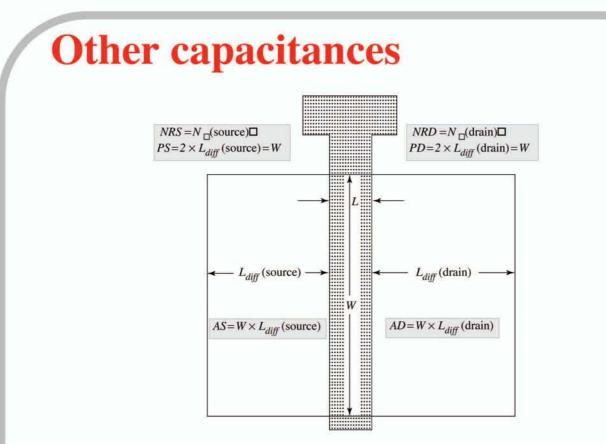
$$\mathbf{C}_{\mathbf{gs},\mathbf{i}} = \frac{\mathbf{dq}_{\mathbf{G}}}{\mathbf{dv}_{\mathbf{GS}}} = \frac{2}{3} \mathbf{WLC}_{\mathbf{ox}}$$

Must add overlap capacitance:

$$C_{gs} = \frac{2}{3} WLC_{ox} + WC_{ov}$$

Gate-to-drain capacitance — only overlap capacitance:

$$C_{gd} = WC_{ov}$$



Source-to-Bulk capacitance:

$$C_{sb} = WL_{diff}C_j + (2L_{diff} + W)C_{jsw}$$

where C_j : Bottom Wall at $V_{SB}(F/cm^2$
 C_{jsw} : Side Wall at $V_{SB}(F/cm)$

Drain-to-Bulk capacitance:

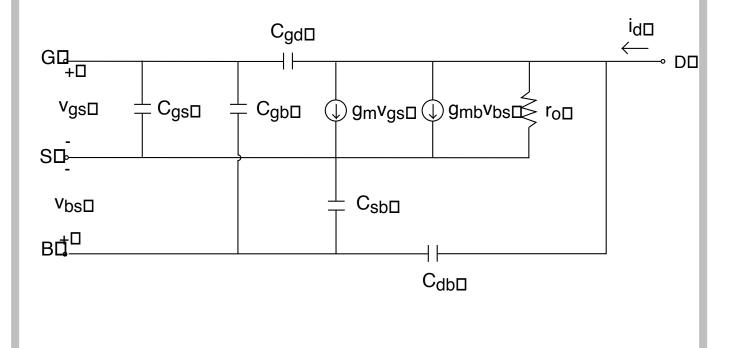
$$C_{db} = WL_{diff} C_j + (2L_{diff} + W)C_{jsw}$$
where C_j : Bottom Wall at $V_{DB}(F/cm^2)$
 C_{jsw} : Side Wall at $V_{DB}(F/cm)$
Gate-to-Bulk capacitance:

 $C_{gb} \equiv$ small parasitic capacitance in most cases (ignore)

What did we learn today?

Summary of Key Concepts

High-frequency small-signal equivalent circuit model of MOSFET



In saturation:

$$g_{m} \propto \sqrt{\frac{W}{L}} I_{D}$$
$$r_{o} \propto \frac{L}{I_{D}}$$
$$C_{gs} \propto WLC_{ox}$$

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